## (19) World Intellectual Property Organization International Bureau

# 

#### (43) International Publication Date 19 July 2001 (19.07.2001)

## PCT

## (10) International Publication Number WO 01/52307 A2

(51) International Patent Classification?:

- (21) International Application Number: PCT/US01/00958
- (22) International Filing Date: 10 January 2001 (10.01.2001)
- (25) Filing Language:

English

HOIL

(26) Publication Language:

English

(30) Priority Data: 09/483,095

14 January 2000 (14.01.2000) US

- (71) Applicant: NUTOOL, INC. [US/US]; 1645 McCandless Drive, Milpitas, CA 95035 (US).
- (72) Inventors: UZOH, Cyprian, E.; 1645 McCandless Drive, Milpitas, CA 95035 (US). TALIEH, Homayoun; 2211 Bentley Ridge Drive, San Jose, CA 95138 (US). BASOL, Bulent; 3001 Maple Avenue, Manhattan Beach, CA 90266 (US), YOUNG, Douglas, W.; 147 North Murphy Avenue, Sunnyvale, CA 94086 (US).
- (74) Agents: JAKOPIN, David, A. et al.; Pilisbury Madison & Sutro LLP, 1100 New York Avenue, N.W., Washington. DC 20005 (US).

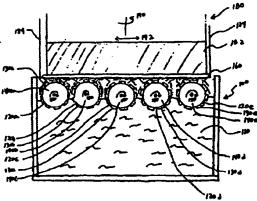
- (81) Designated States (national): AB, AG, AL, AM, AT, AU, AZ, BA, BB, BO, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FL, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, UZ, VN, YU, ZA, ZW.
- (84) Designated States (regional): ARIPO patent (GH. GM. KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Burasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

#### Published:

without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: SEMICONDUCTOR WORKPIECE PROXIMITY PLATING METHODS AND APPARATUS



(57) Abstract: The present invention relates to methods and apparatus for plating a conductive material on a semiconductor substrate by rotating pad or blade type objects in close proximity to the substrate, thereby eliminating/reducing dishing and voids. This is achieved by providing pad or blade type objects mounted on cylindrical anodes or rollers and applying the conductive material to the substrate using the electrolyte solution disposed on or through the pads, or on the blades. In one embodiment of the invention, the ped or blade type objects are mounted on the cylindrical anodes and rotated about a first axis while the workpiece may be stationary or rotate about a second axis, and metal from the electrolyte solution is deposited on the workpiece when a potential difference is applied between the workpiece and the anode. In another embodiment of the present invention, the plating apparatus includes an anode plate spaced apart from the cathode workpiece. Upon application of power to the anode plate and the cathode workpiece, the electrolyte solution disposed in the plating apparatus is used to deposit the conductive material on the workpiece surface using cylindrical rollers having the pad or blade type objects.